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Please download the latest datasheet of CSTCE16M0V53-R0 from the official website of Murata

CSTCE16M0V53-R0







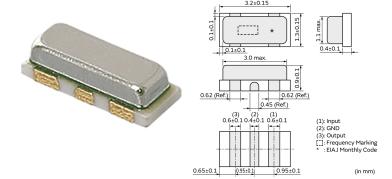


Applications

Unsuitable	Please be sure to read and comply with	
Applications	these "Precautions for use."	
	Consumer equipment	
	Please refer to Our Website and	
	specifications, etc. for information about	
Specific	the performance, functions, quality,	
'	management, and safety required for	
Applications	the above applications, and use	
	Products after confirming the	
	performance and reliability of the actual	
	Product.	



Appearance & Shape





Packaging Information

Packaging	Specifications	Standard Packing Quantity
R0	180mm Embossed Tape	3000



Features

MURATA's package technology expertise has enabled the development of the Chip CERALOCK.

High-density mounting is made possible by the small package and the elimination of the need for an external load capacitor.

Features

- 1. Oscillation circuits do not require external load capacitors.
- 2. Available for a wide frequency range.
- 3. Extremely small and have a low profile.
- 4. No adjustment is necessary for oscillation circuits.

1 of 2

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2. This datasheet has only typical specifications because there is no space for detailed specifications.

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering





Product Search Data Sheet

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CSTCE16M0V53-R0



Specifications

Product Type	Ceramic Resonator (CERALOCK)
Series	CSTCE_V
Frequency	16.000MHz
Frequency Tolerance	+/-0.50% max.
Operating Temperature Range	-20°C~80°C
Frequency Shift by Temperature	+/-0.30% max.
Frequency Aging	+/-0.20% max.
Resonant Impedance (R1)	40ohm max.
Built-in Load Capacitance (CL1/CL2)	15pF
Shape	SMD
Wash	Not available
L x W (size)	3.2x1.3mm
Mass	17mg

2 of 2

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